

A2

This application is related to Patent Application No. 09/608,097 having attorney docket no. M-8321 US filed June 30, 2000, and entitled "CONCATENATION DETECTION ACROSS MULTIPLE CHIPS," having Douglas E. Duschatko, Lane Byron Quibodeaux, Robert A. Hall, Andrew J. Thurston as inventors. This application is assigned to Cisco Technology, Inc., the assignee of the present invention, and is hereby incorporated by reference, in its entirety and for all purposes.

Please replace the paragraph starting on page 2, line 1 with the following replacement paragraph.

A3

This application is related to Patent Application No. 09/607,912 having attorney docket no. M-8320 US filed June 30, 2000, and entitled "PATH AIS INSERTION FOR CONCATENATED PAYLOADS ACROSS MULTIPLE CHIPS," having Douglas E. Duschatko, Lane Byron Quibodeaux, Robert A. Hall, Andrew J. Thurston as inventors. This application is assigned to Cisco Technology, Inc., the assignee of the present invention, and is hereby incorporated by reference, in its entirety and for all purposes.

Please replace the paragraph starting on page 2, line 8 with the following replacement paragraph.

A4

This application is related to Patent Application No. 09/608,461 having attorney docket no. M-8340 US filed June 30, 2000, and entitled "CHANNEL ORDERING FOR COMMUNICATION SIGNALS SPLIT FOR MATRIX SWITCHING," having Douglas E. Duschatko, Lane Byron Quibodeaux, Robert A. Hall, Andrew J. Thurston as inventors. This application is assigned to Cisco Technology, Inc., the assignee of the present invention, and is hereby incorporated by reference, in its entirety and for all purposes.

In accordance with 37 CFR § 1.121(b)(1)(iii), Attachment B contains marked up versions of the replacement paragraphs illustrating the newly introduced changes in the specification.

LAW OFFICES OF  
SKJRVEN MORRILL  
MACPHERSON LLP

25 METRO DRIVE  
SUITE 700  
SAN JOSE, CA 95110  
(408) 453-9200  
FAX (408) 453-7979